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Karen Vertz	(Depositor's name)
Karen Vertz	(Signature)
12-7-05	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/015,741	12/17/2001	Suresh Balasubramanian	TI-33469/TXN-022	7022

TITLE OF INVENTION: GENERATING A LOCK SIGNAL INDICATING WHETHER AN OUTPUT CLOCK SIGNAL GENERATED BY A PLL IS IN LOCK WITH AN INPUT REFERENCE SIGNAL

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	03/01/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
COX, CASSANDRA F	2816	327-049000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1. Alan K. Stewart

2. W. James Brady, III

3. Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Dallas, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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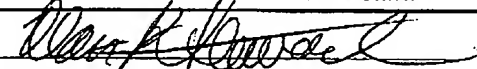
5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date

12/7/05

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Registration No.

35,373

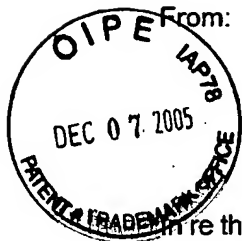
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of

Applicant **Suresh Balasubramanian**

Docket Number: **TI-33469**

Serial No.: **10/015,741**

Art Unit: 2816

Filed: 12/17/01

Examiner: **Cassandra F. Cox**

For: **Generating a Lock Signal Indicating Whether an Output
Clock Signal Generated by a PLL is in Lock with an Input
Reference Signal**

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile
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Karen Vertz
Karen Vertz

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<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Suresh Balasubramanian	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/015,741 Filing Date: 12/17/01	
TITLE OF INVENTION: Generating a Lock Signal Indicating Whether an Output Clock Signal Generated by a PLL is in Lock with an Input Reference Signal	
TI FILE NO.: TI-33469	DEPOSIT ACCT. NO.: 20-0668
FAXED: 12-7-05 DUE: 3-1-06 ATTY/SECY: AKS/kjv	

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